

# Kamalaselvan A

## Curriculum Vitae

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### Career Objective

To Secure a Position with Opportunities to Contribute My Education, Skill Set and Experiences to the Organization in Terms of Team player, Teaching, Learning and Research Using Qualitative and Quantitative Approaches.

### Educational qualification

2014-2022	Ph.D. SASTRA University, Thanjavur ,India.
2011-2013	M.E.,- Power Electronics and Drives, Saranathan College of Engineering, Trichirappalli, India.
2004-2008	B.E., - EEE, A.R.J. College of Engineering, Mannargudi, India.
2004	XII - Government Boys Higher Secondary School Pattukkottai.
2001	X - Findlay Higher Secondary School, Mannargudi ,India.

### Experience

#### Academic

May 2022- till now	Assistant Professor in Mohamed Sathak A.J. College of Engineering, Chennai ,India.
Oct 2014- Mar 2022	(PhD) Teaching Assistant in SASTRA University, Thanjavur ,India.
Dec 2013-Jun 2014	Assistant Professor in Sir Issac Newton College of Engineering, Nagapattinam ,India.
July 2013-Nov 2013	Assistant Professor in A.R.J. College of Engineering, Mannargudi, India.

#### Industrial

Feb 2010 to Jul 2011	Testing and Commissioning Engineer in Voltech Engineers Pvt Ltd, T-nagar, Chennai
Jun 2008 to Jan 2010	Erection and Commissioning Engineer in Sudhan Power Tech Pvt Ltd, Alandur, Guindy, Chennai

## Publications

1. **A.Kamalaselvan** Balasubramanian Madanmohan and Rajesh Rajaman, “Extended Application for the Impulse-Based Frequency Response Analysis: Preliminary Diagnosis of Partial Discharges in Transformer ”, *IEEE Access*, 2020,
2. **A.Kamalaselvan** Rajesh Rajamani and Muthaiah Rajappa, “Extended Use for the Frequency Response Analysis: Switching Impulse Voltage Based Preliminary Diagnosis of Potential Sources of Partial Discharges in Transformer ”, *Applied Sciences*, 2020,
3. **A.Kamalaselvan** Rajesh Rajamani and Muthaiah Rajappa, “Interturn short diagnosis in small transformers through impulse injection: on-line on-load self –impedance transfer function approach ”, *IET Science, Measurement and Technology*, 2017,
4. **A.Kamalaselvan** and Muthaiah Rajappa, “Condition Assessment and Diagnosis of winding insulation in Static and Rotating Machines Using Frequency Response Analysis – A Survey ”, *IJMET*, 2017
5. **A.Kamalaselvan** and Lenin Prakash, “Modeling Simulation and Analysis of Closed Loop Speed Control of PMSM Drive System ”, *IEEE. conf, DOI.10.1109/ICCPCT.2014.7055050*

## Technical Proficiency

- 1 Able to develop LABVIEW based hardware applications
- 2 Circuit simulation and modeling in Matlab simpower systems
- 3 ETAP power system simulation
- 4 Quick field Electromagnetic field simulation
- 5 Circuit simulation and PCB layout in National instrument’s circuit design suit11
- 6 Able to draw 2D drawings in Autocad 2014
- 7 Programming DSP processor using code composer studio
- 8 Hands on experience in handling TMS320F2812 processor
- 9 Able to do PCB routing design and fabrication
- 10 Proficient in using Eagle and KICAD PCB design software

## Area of Interest

- 1 High Voltage Engineering
- 2 Power Electronics and Drives
- 3 Electrical Machines
- 4 Dc and Ac Drives
- 5 Circuit Theory

## Industrial Projects Worked

- 1000MW Nuclear power project having 3500 valve motors had been commissioned at Kudankulam, Tuticorin.
- 220kV/66kV switchyard having a total of 15 bays at GETCO Lakhani, Gujarat.
- 220kV switchyard having a 2 no of bays at Tata Motors Ltd, Ahmadabad.
- Erection and Commissioning of Snam Alloys Pvt Ltd, Ferro alloy manufacturing plant at Pakala, Andhra Pradesh.
- Erection and Commissioning of Fritzmeier Motherson Cabin Engineering Ltd, at Chinnayan Chatram Sriperumbudur, Chennai.

## NPTEL Online Course

- Successfully completed course on "Introduction to Machine Learning"

## Academic Project

### Master Degree – Project

Title: Simulation and Digital Implementation of closed loop speed control of PMSM

### Project Outline:

An attempt is made to implement closed loop vector control scheme of PMSM drive in hardware. The implementation in hardware has been accomplished in subsystem level, which includes Hysteresis current controller of Voltage Source Inverter, digital implementation of inverse park's transformation and speed sensing using DSP TMS320F2812 Micro-controller. It can be expected all these hardware implementations, which have been done individually in this work, once implemented together with a coordinated control, would lead to a successful implementation of the complete PMSM drive system. That the developed simulation model can be an easy to design tool for the design and development of PMSM drives for different control algorithms and topological variations with reduced computation time and memory size.

## Bachelor Degree – Project

Title: Electromagnetic Braking for Automobiles

### Project Outline :

The objective of the work is to prevent accidents. The device will be activated when the speed is greater than the preset speed (Above which driver can't stop suddenly) and it is deactivated when it is below the preset speed or when the vehicle is in traffic hence the control is given to driver. When the device is activated and in case of accident the vehicle stops automatically with respect to the speed and distance between the vehicles and objects. The brake is applied linearly according to the distance between the vehicles. In different zones like school and speed limit zone, when the vehicle is moving at a speed greater than the limited, then the device gets activated. It will control the speed increase and bring it to the required speed and it gets deactivated. So that the driver can maintain the speed.

## Seminar and Workshop Attended

1. Machine learning with Matlab, Mathwork Training Services, Feb 2021
2. Deep learning with Matlab, MathWork Training Services, Feb 2021
3. Workshop on mathematical modelling and its application to Electrical Engineering, SSN, Chennai, 2021
4. Novel Designs and control strategies and innovative technical practices, AICTE sponsored six day online STTP, SEC, Trichy, 2020
5. Electromagnetic Field Computation and Modeling Using FEM Packages, JJ College of Engineering and Technology, Trichy, Oct 25 2013
6. Two Days Skill Development Workshop on PCB Routing Design and Fabrication, Saranathan College of Engineering and Technology, Trichy, May 4 and 5 2013
7. Application of Biological Inspired Algorithms by Power Systems and Power Electronics Engineering, National Institute of Technology, Trichy, July 28 2012
8. Analog circuit Design Organisation as part of WAVES 06, Anna University, Chennai

## Personal Details

Name : A. Kamalaselvan  
Father's Name : Arunachalam  
DOB: 19 may 1986  
Gender: Male  
Marital Status: Married  
Nationality: Indian  
Religion: Hindu  
Languages Known: Tamil, English  
Total Work Experience: 10, 6 Years of research, 3 years of Industrial and 1 year of Academic  
Resi Add : S5, Block-2, Sai Arthi, Dhanalakshmi Avenue, 4th Street VGP Prabhu Nagar, Perumbakkam, Chennai Pin: 600100.

## References

1. **Dr. R.Rajesh**, (High Voltage Lab-In-Charge)  
Senior Assistant professor, Dep of Electrical Engineering,  
SASTRA Deemed University,  
Thanjavur-613401  
Mail id: rajesh@eee.sastra.edu, Mob: 9486091369
2. **Dr. M.Balasubramanian**,  
Senior Assistant professor, Dep of Electrical Engineering,  
SASTRA Deemed University,  
Thanjavur-613401  
Mail id: mbsmanian16@eee.sastra.edu, Mob: 9789160805
3. **Dr .S.P.Balaji** ,  
Principal Engineer,  
EMTP KNR ENGINEERS (INDIA) PVT LTD (A JEF GROUP COMPANY),  
Chennai-600035  
Mail id: hv.spbalaji@gmail.com,mob: 9840512674

## Declaration

I hereby declare that all the above furnished information is true to the best of my knowledge.

Date:

Place: Chennai